

IC CARD

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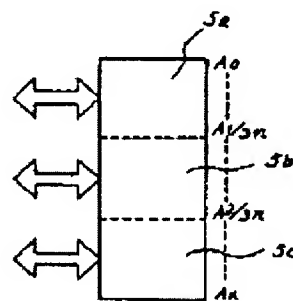
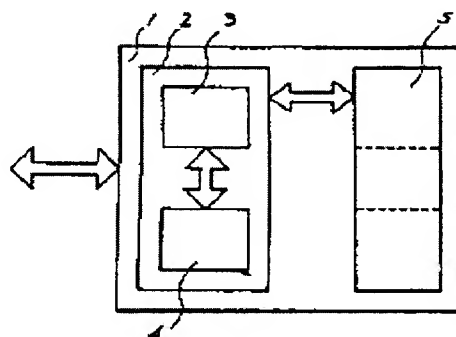
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Abstract of JP63108445

PURPOSE: To prevent the access of all pieces of information stored in an IC card with one identifying code only by dividing and storing the information stored into the IC card and setting the identifying data to attain the access of the divided information at a divided unit. **CONSTITUTION:** In a memory element 4 of a CPU 2, addresses A0-An corresponding to the memory information area of a memory element 5 are divided into three respective areas 5a, 5b and 5c and identifying codes 11, 22 and 33 corresponding to the division are stored. When the identifying code is inputted from the external part to an IC card 1, MPU 3 investigates to the



memory element 4 whether or not the identifying code exists, and controls how the addressing at the time of presence comes to be. Since the information of the identifying code 11 is set to the address of A0-A1/3n of the area 5a, it is judged that the identifying code is coincident. Consequently, only the area 5a of the A0-A1/3n of the memory element 5 can be accessed and the access to the A1/3n+1-An cannot be executed.

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